

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

10/009196

APPL NUM 10009196	FILING DATE 04/15/2002	CLASS 438	SUBCLASS 2	GAU 2847	EXAMINER GEYER
**APPLICANTS: Saijo Kinji; Yoshida Kazuo; Okamoto Hiroaki; Ohsawa Shinji; <div style="text-align: right;">SRG</div>					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A 371 OF PCT/JP00/03746 05/09/2000 <div style="text-align: right;">SRG</div>					
** FOREIGN APPLICATIONS VERIFIED: JAPAN 11/164,454 06/10/1999 <div style="text-align: right;">SRG</div>					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO	
Verified and Acknowledged Examiners's initials		SRG		ASISO=7	
TITLE : Clad plate for forming interposer for semiconductor device, interposer for semiconductor device, and method of manufacturing them					
U.S. DEPT. OF COMM. / PAT. & TM. / PTO-436L (Rev. 12-95)					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims <input checked="" type="checkbox"/>	Print Claim for O.G. <input checked="" type="checkbox"/>
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg. <input checked="" type="checkbox"/>	Figs. Drwg. <input checked="" type="checkbox"/> Print Fig. <input checked="" type="checkbox"/>
		Primary Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
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